Subject: 35th FCAL Hardware WG Meeting

Resent-Date: Wed, 17 Jan 2018

Resent-From: <fcal-members@cern.ch>

Dear All,

A short summary from the 35th HWG meeting:

- Sasha presented the results of calibration of APV25 chip used during the last test beam

 -- Since the chip has small input range (~8 MIPS) a capacitive charge divider was used to increase its input range (by factor of ~4)

 -- Calibration curves (Vout/Qin) were measured with and without the divider and significant non-linearity (expected) was measured

 -- One issue was found: for large input signals there is a "strange" crosstalk.

"Strange" because it is not in neighbor channel but in a channel distant by ~32 channels.

Under investigation, not yet understood

 -- In general the data quality should be good enough to make interesting physics analyses.

Firsts analyses have been already done. Results will be shown at CLICdp workshop.

- Unfortunately, for the FCAL, Strahinja will change the job from the beginning of February, and will not work for FCAL.

First, the Collaboration appreciates a lot  Strahinja's great work/contribution for the FCAL.

Second, we need to find a person(s) who will overtake various tasks from Strahinja.

Best Regards,

Marek