Dear All,

Short summary from the 29th FCAL HWG Meeting:

1. I have shown the status of works on compact forward calorimeter which I presented 2 weeks ago during AIDA-2020 Annual Meeting in Paris

- one of the AIDA-2020 milestones (deadline April 2017) was readout ASICs and readout board for forward calorimeter

- for the above reason I presented more in detail the design and measurements of our ASICs and the status of works on FPGA-based readout

2. Yan  informed us about incoming meeting of ILD conveners in France. Yan will give talk about hardware activities and Bogdan should prepare the talk about software activities (Bogdan will not participate so somebody else will present it)

3. We were discussing about 25 new tungsten plates which are just being ordered. Misha presented the status - the boards of good quality should be ready sometime in the summer. Konrad will send Misha the most recent documentation of the boards.

 Best Regards,

  marek